

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	3374	"first in" near3 "first out"	USPAT	OR	OFF	2006/09/11 16:47
L2	7	"low power SDRAM"	USPAT	OR	OFF	2006/09/11 16:46
L3	966	L1 near3 memory	USPAT	OR	OFF	2006/09/11 16:49
L4	5	"ddr-I sdram"	USPAT	OR	OFF	2006/09/11 16:50
L5	7	"ddr I sdram"	USPAT	OR	OFF	2006/09/11 16:50
L6	1410	"ddr sdram"	USPAT	OR	OFF	2006/09/11 17:05
L7	49	"ddr ii sdram"	USPAT	OR	OFF	2006/09/11 17:07
L8	418	"data delay circuit"	USPAT	OR	OFF	2006/09/11 17:22
L9	427	"off chip driver"	USPAT	OR	OFF	2006/09/11 17:30
S1	1154	"tri-state output"	USPAT	OR	OFF	2006/09/06 20:54
S2	69	S1 and "711".clas.	USPAT	OR	OFF	2006/09/06 20:54

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	2915438	rise or rising or first	USPAT	OR	OFF	2006/09/06 20:27
S2	127490	S1 near3 edge	USPAT	OR	OFF	2006/09/06 20:27
S3	2927152	rise or rising or first	USPAT	OR	ON	2006/09/06 20:27
S4	151570	S3 near3 edge	USPAT	OR	ON	2006/09/06 20:27
S5	44541	clock near3 cycle	USPAT	OR	ON	2006/09/06 20:27
S6	3217	S4 with read	USPAT	OR	ON	2006/09/06 20:28
S7	1574	S5 and S6	USPAT	OR	ON	2006/09/06 20:28
S8	537	S5 same S6	USPAT	OR	ON	2006/09/06 20:28
S9	94	S8 and "711".clas.	USPAT	OR	ON	2006/09/06 20:33
S10	2851255	fall or falling or second	USPAT	OR	ON	2006/09/06 20:33
S11	130711	S10 near3 edge	USPAT	OR	ON	2006/09/06 20:34
S12	2159	S11 with read	USPAT	OR	ON	2006/09/06 20:34
S13	948	S5 and S12	USPAT	OR	ON	2006/09/06 20:35
S14	342	S5 same S12	USPAT	OR	ON	2006/09/06 20:35
S15	52	S14 and "711".clas.	USPAT	OR	ON	2006/09/06 20:35